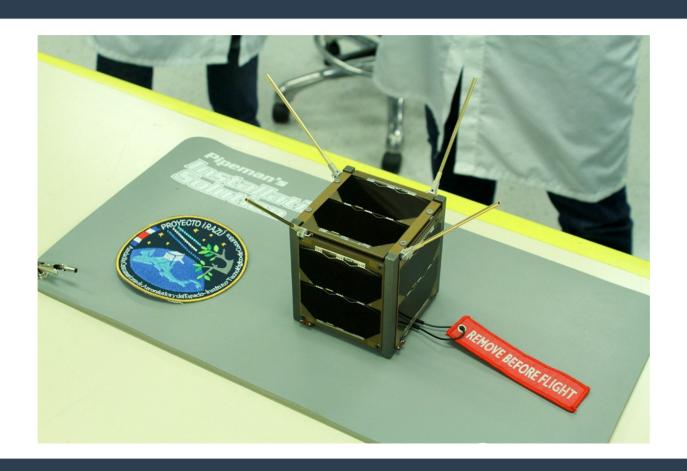
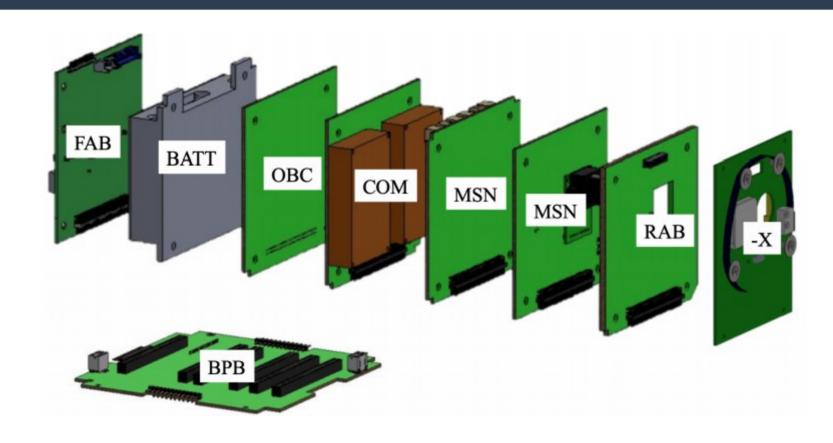
Arenal: Single Board OBC+COMs module

Jairo Rodríguez Olman Quirós

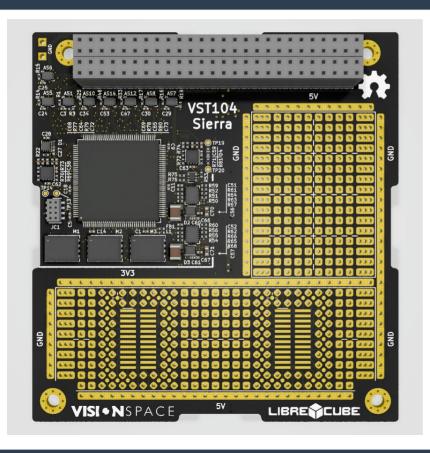
Irazú Project

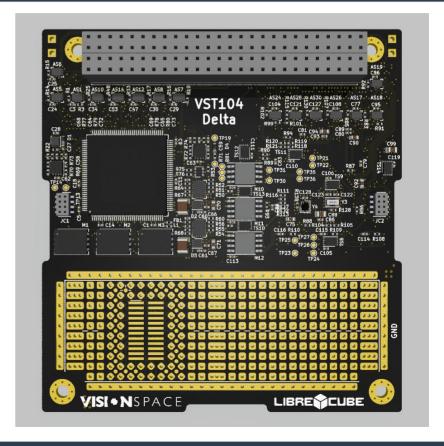


BIRDSBus



VST104





STM32WL LoRa SoC

Control

Power supply 1.8 to 3.6 V w/ DCDC+ LDO POR/PDR/PVD/BOR

Crystal oscillators 32 MHz (Radio + HSE) 32.768 KHz (LSE)

Internal RC oscillators 32,768 KHz + 16 MHz + 48 MHz ± 1% acc. over V and T(°C)

RTC/AWU/CSS

PLL

SysTick timer

2 watchdogs (WWDG/IWDG)

43 GPI0s

Cyclic redundancy check

Voltage scaling (2 modes)

Arm® Cortex®-M4 DSP 48 MHz

Nested vector interrupt controller (NVIC)

Memory protected unit (MPU) JTAG/SW debug

ART Accelerator™

AHB Bus matrix

2x DMA 7 channels

Radio

LoRa®, (G)FSK, (G)MSK, BPSK

+15dBm & +22dBm Power Outputs -148 dBm sensitivity

(LoRa) 150 MHz to 960 MHz Memory

Up to 256-Kbyte Flash

Up to 64-Kbyte SRAM

CM4 or CM0 Boot_Lock

Boot loader

Hide protect

Timers

1 x 32-bit timer

3x 16-bit timers 3x ULP 16-bit timers

Analog

1x 12-bit ADC SAR 2.5 Msps

12-bit DAC

2x ULP comparators

Temperature sensor

Security

AES 256-bit + TRNG + PCROP

Tamper Detection
Secure Areas

Secure FW Install

Debug control

Boot Selection

Secure Sub-GHz, MAC Layer, SFI

Key Management services

Connectivity

2x SPI, 3x I2C

2x USART LIN, smartcard, IrDA, Modem control

1x ULP UART

Flash memory / RAM size (bytes)



Arm® Cortex®-M0+

48 MHz

Nested vector

interrupt controller

(NVIC)
Memory protected unit (MPU)

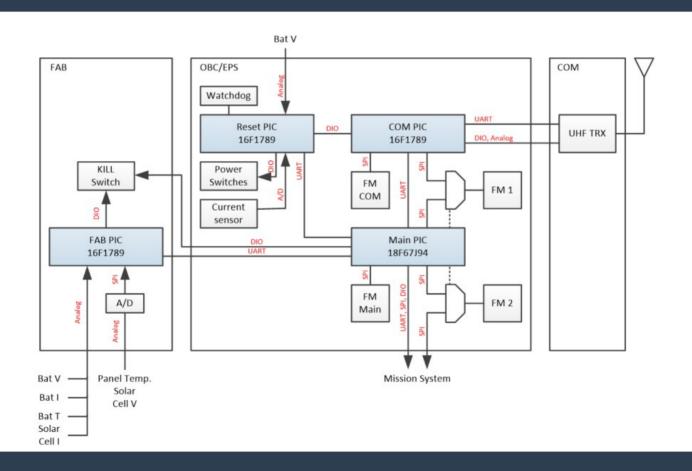
SW debug

Legend:

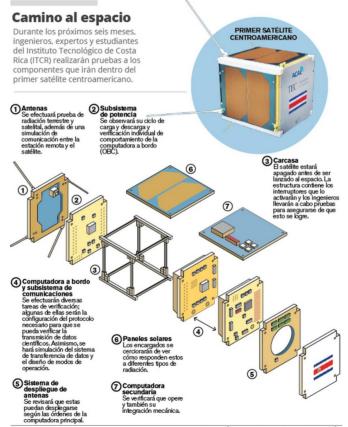
LoRa®, (G)FSK, (G)MSK, BPSK

FSK, (G)FSK, (G)MSK, BPSK

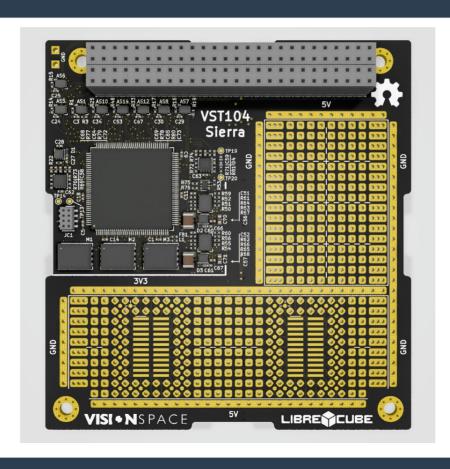
Case: BIRDSBus



Case: Irazú



VST104 Sierra



Software Stack

Additionally a small software stack is being developed to offer a compatible set of services out of the box.

Implementation using Rust programming language, to leverage on safty features.

Planed services:

- Telemetry
- Commands
- Flight Planning
- Routing of Packages to/from other subsystems.

First concept to be released soon (open-source off course;))

We need your feedback!

Do you think this board would be useful?

Please, let us know what you think!

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olmanqj@tutamail.com

